U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,634,023	10-2003	Komatsu et al.	717/159
*	B	US-6,539,541	03-2003	Geva, Robert Y.	717/150
*	C	US-5,913,925	06-1999	Kahle et al.	712/206
*	D	US-7,152,170	12-2006	Park, Gi-ho	713/320
*	E	US-7,143,401	11-2006	Babaian et al.	717/149
*	F	US-7,082,601	07-2006	Ohsawa et al.	717/149
*	G	US-7,010,787	03-2006	Sakai, Junji	717/160
*	H	US-6,961,930	11-2005	Waldspurger et al.	717/141
*	I	US-6,961,925	11-2005	Callahan et al.	717/128
*	J	US-6,367,070	04-2002	Haghighat et al.	717/160
*	K	US-6,964,043	11-2005	Wu et al.	717/160
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Cui et al, "Parallel replacement mechanism for multithread", IEEE, pp 338-341, 1997
	V	Li et al, "A framework of reachability testing for Java multithread programs", IEEE, pp 2730-2734, 2004
	W	Matsuzaki et al, " A multithread processor architecture based on the continuation model", IEEE IWIA, pp 1-8, 2005
	X	Manson et al, "Core semantics of multithreaded Java", ACM JAVA pp 29-38, 2001

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

		Application/Control No.	Applicant(s)/Patent Under Reexamination
Notice of References Cited		10/782,674	ILEY ET AL.
		Examiner	Art Unit	
		Anil Khatri	2191	

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Tardieu et al, "Scheduling independent threads and exceptions in SHIM" ACM EMSOFT, pp 142-151, 2006
	V	Schwan et al, "Multiprocessor real time threads", ACM SIGOPS, vol. 26, issues 1, pp 54-65, 1992
	W	Serrano et al, "Scheme fair threads", ACM PPDP, pp 203-214, 2004
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.